



FIG. 2

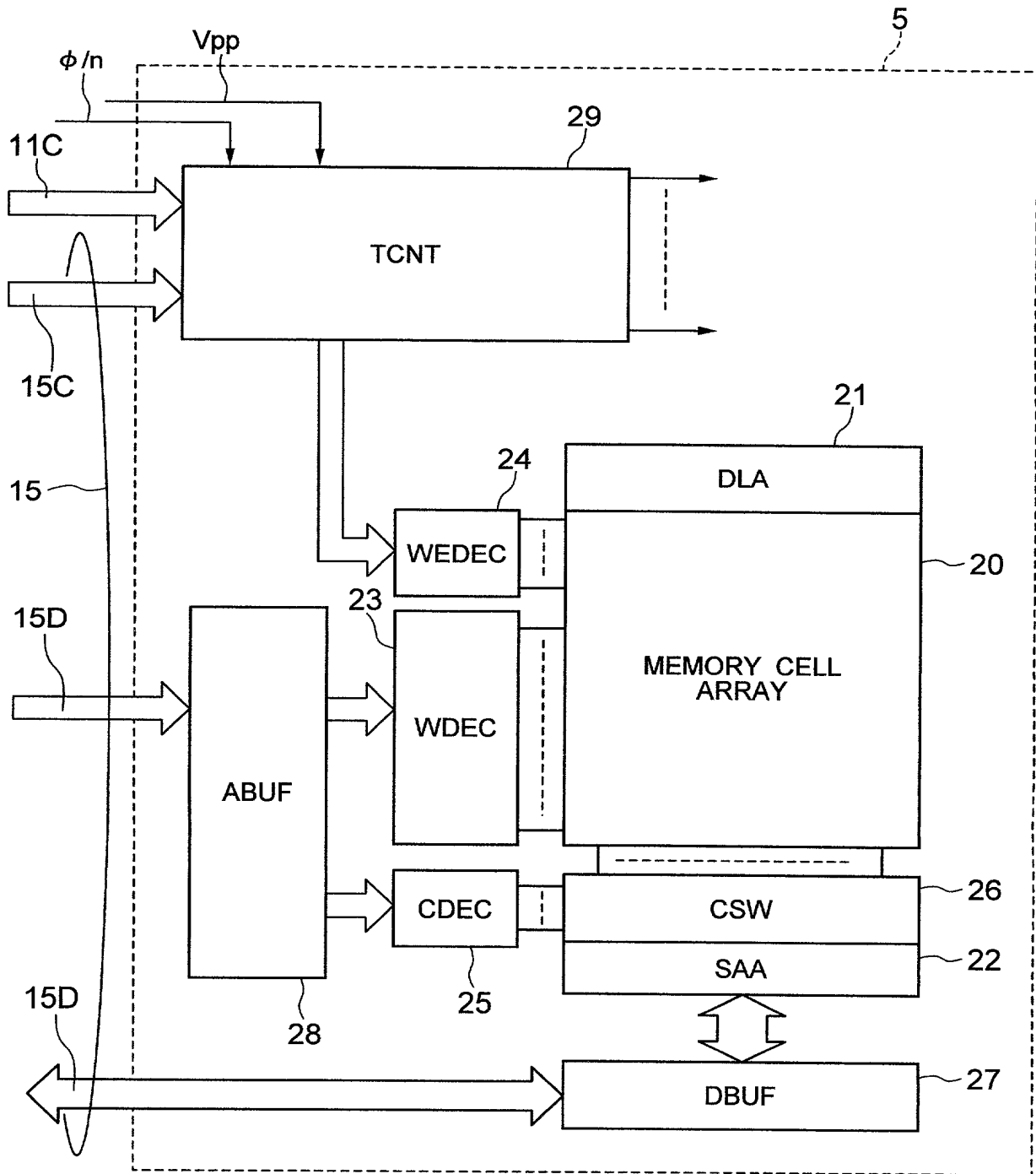
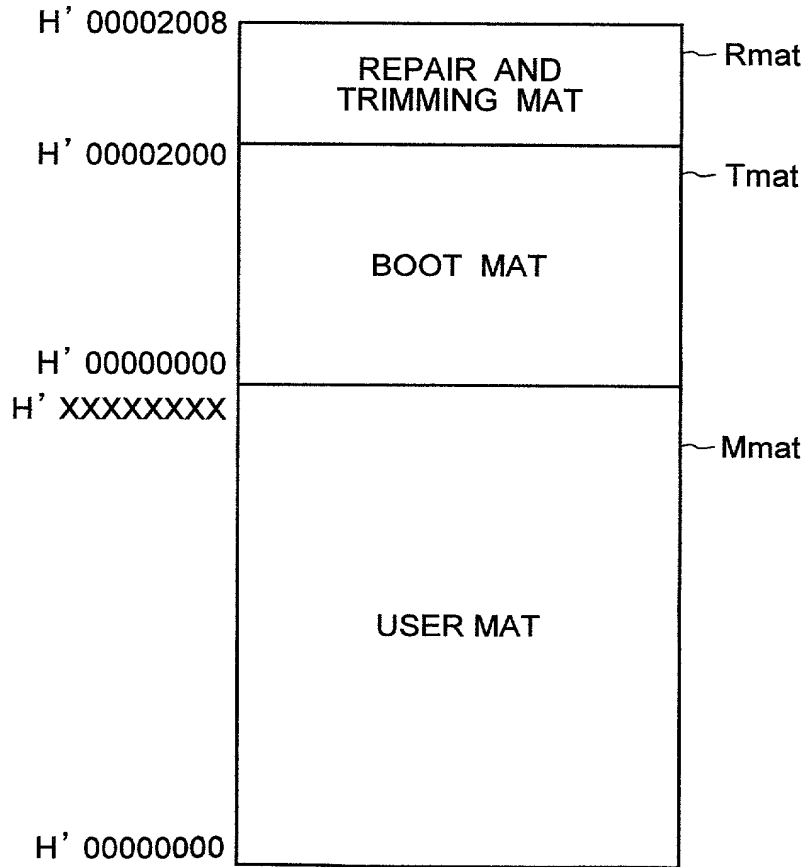


FIG. 3



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FIG. 4

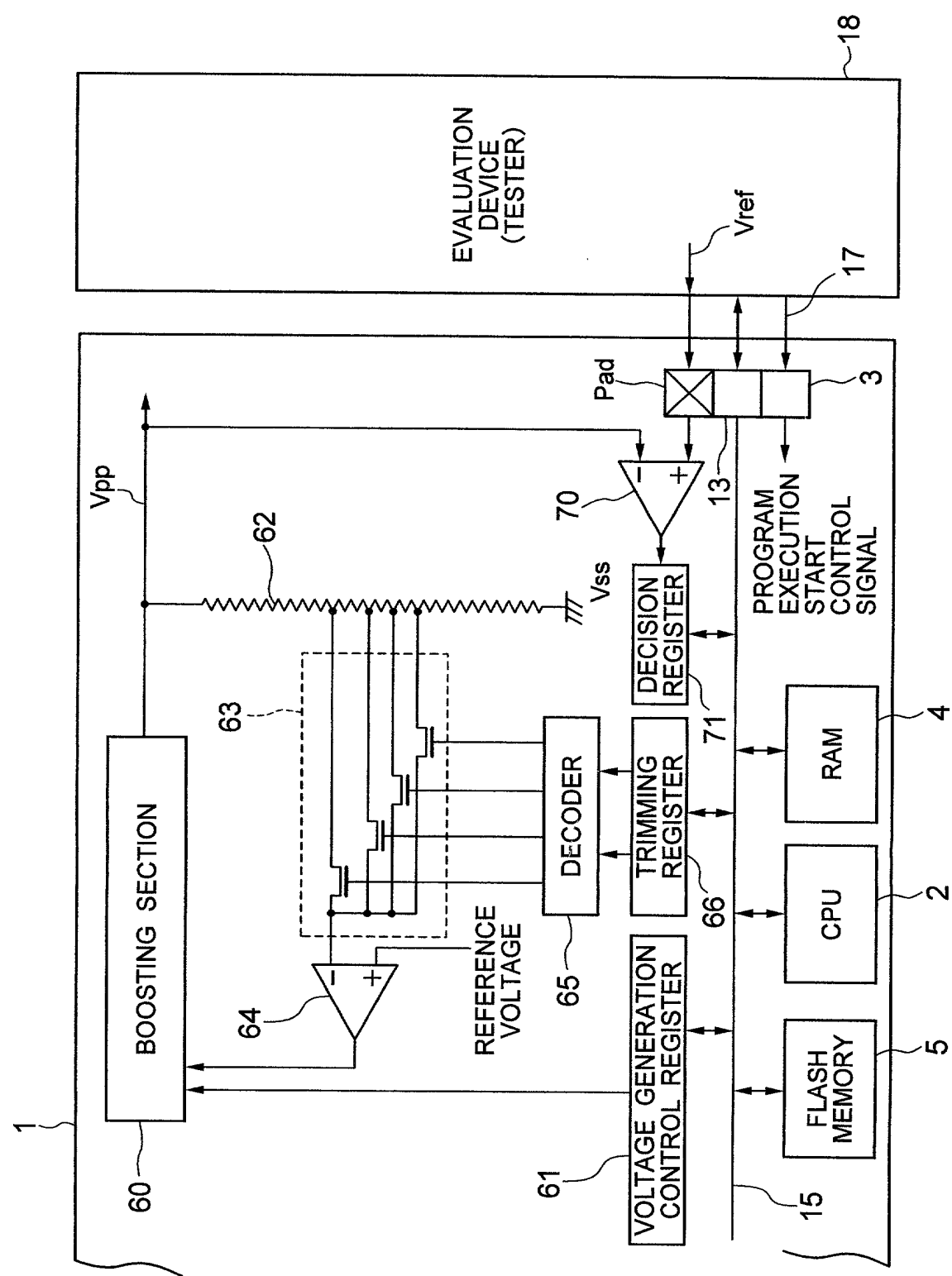


FIG. 5

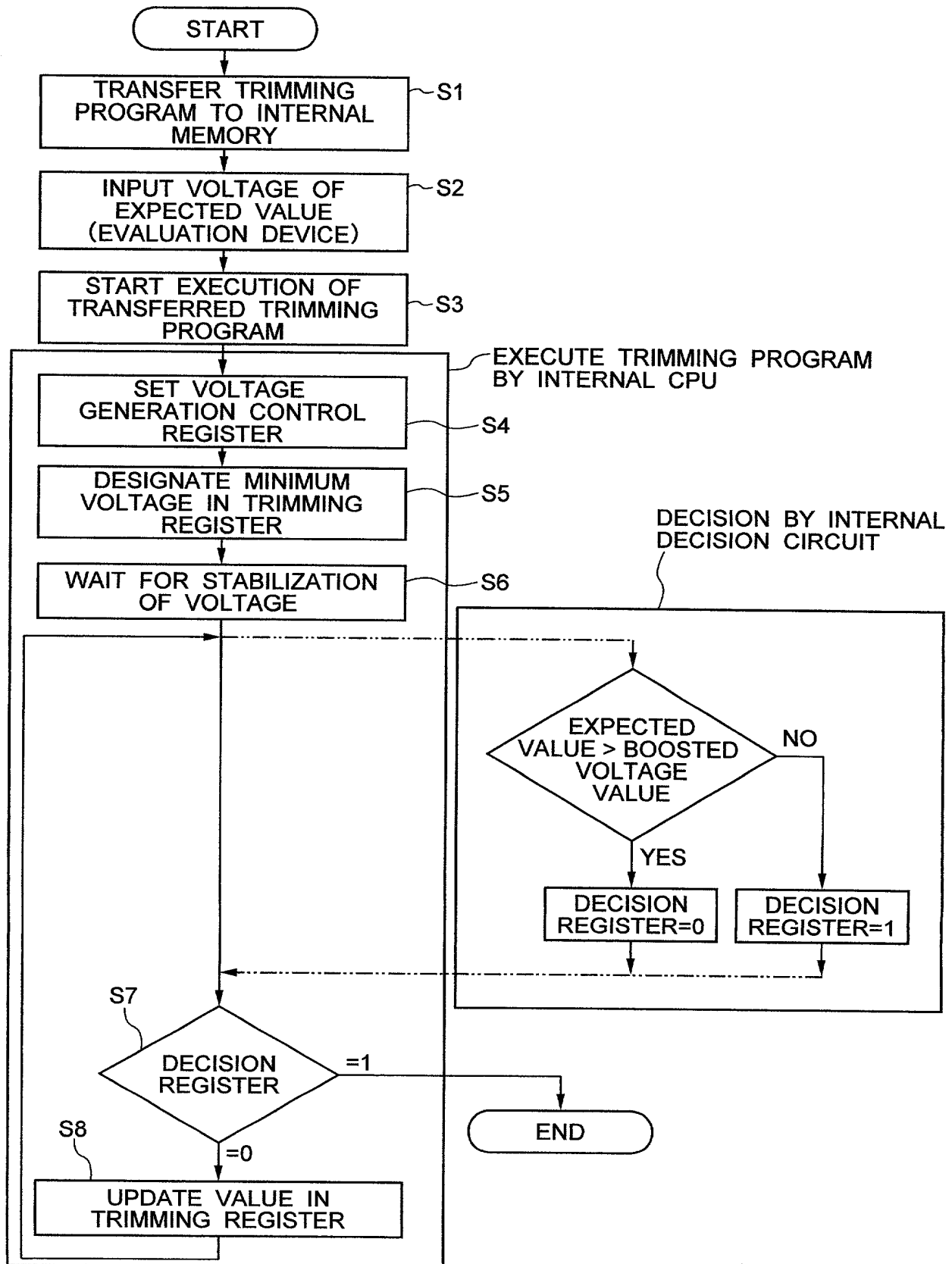


FIG. 6

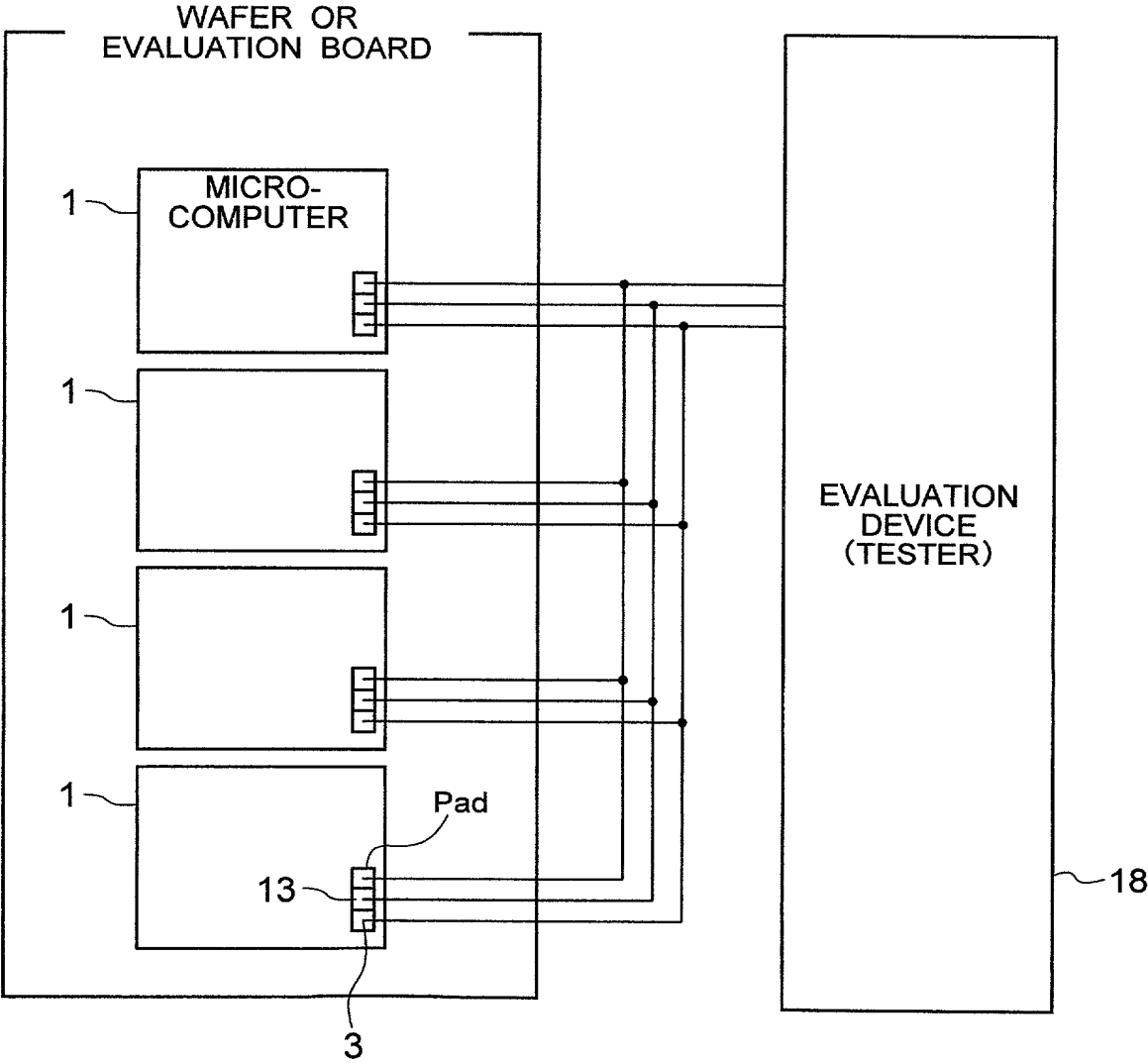


FIG. 7

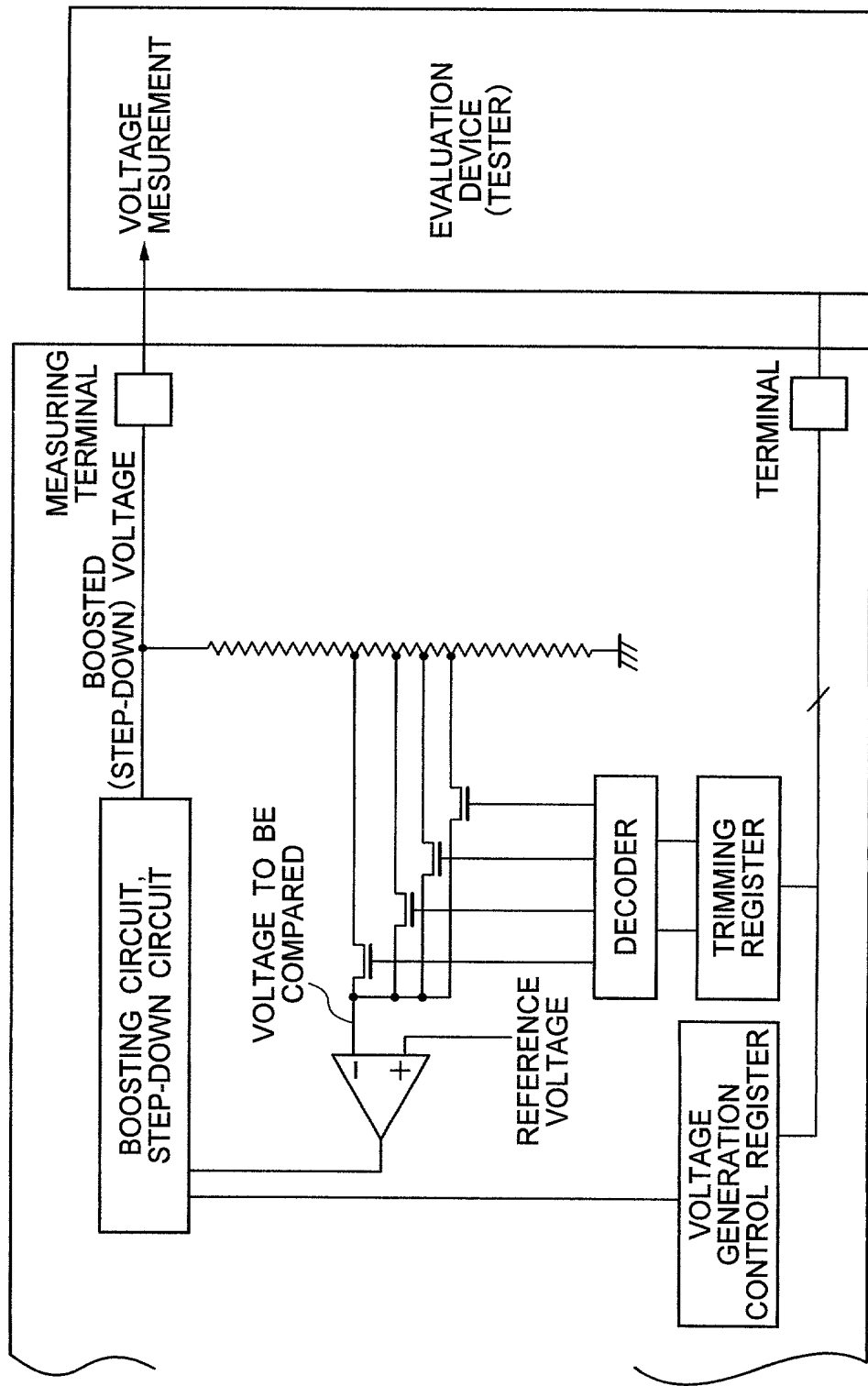
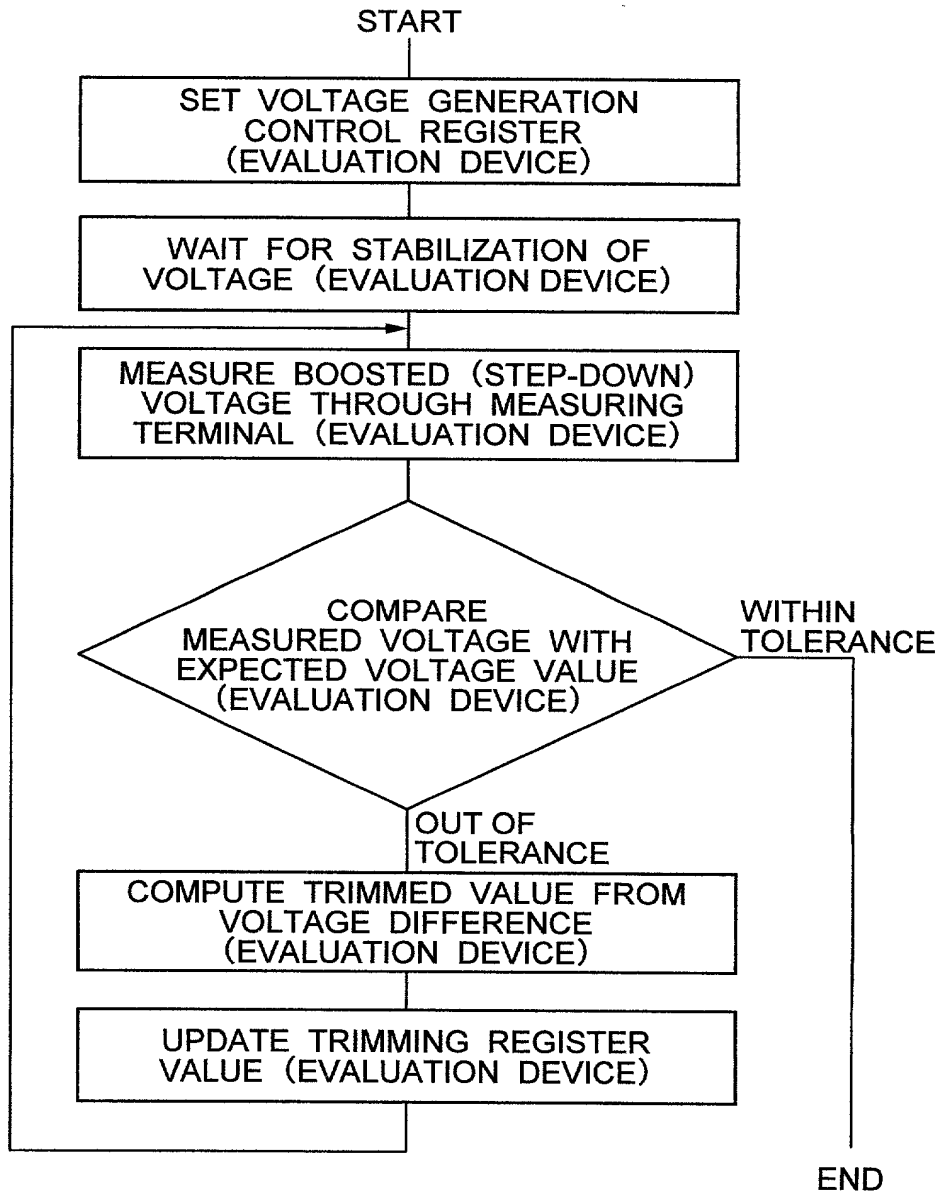


FIG. 8



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FIG. 9

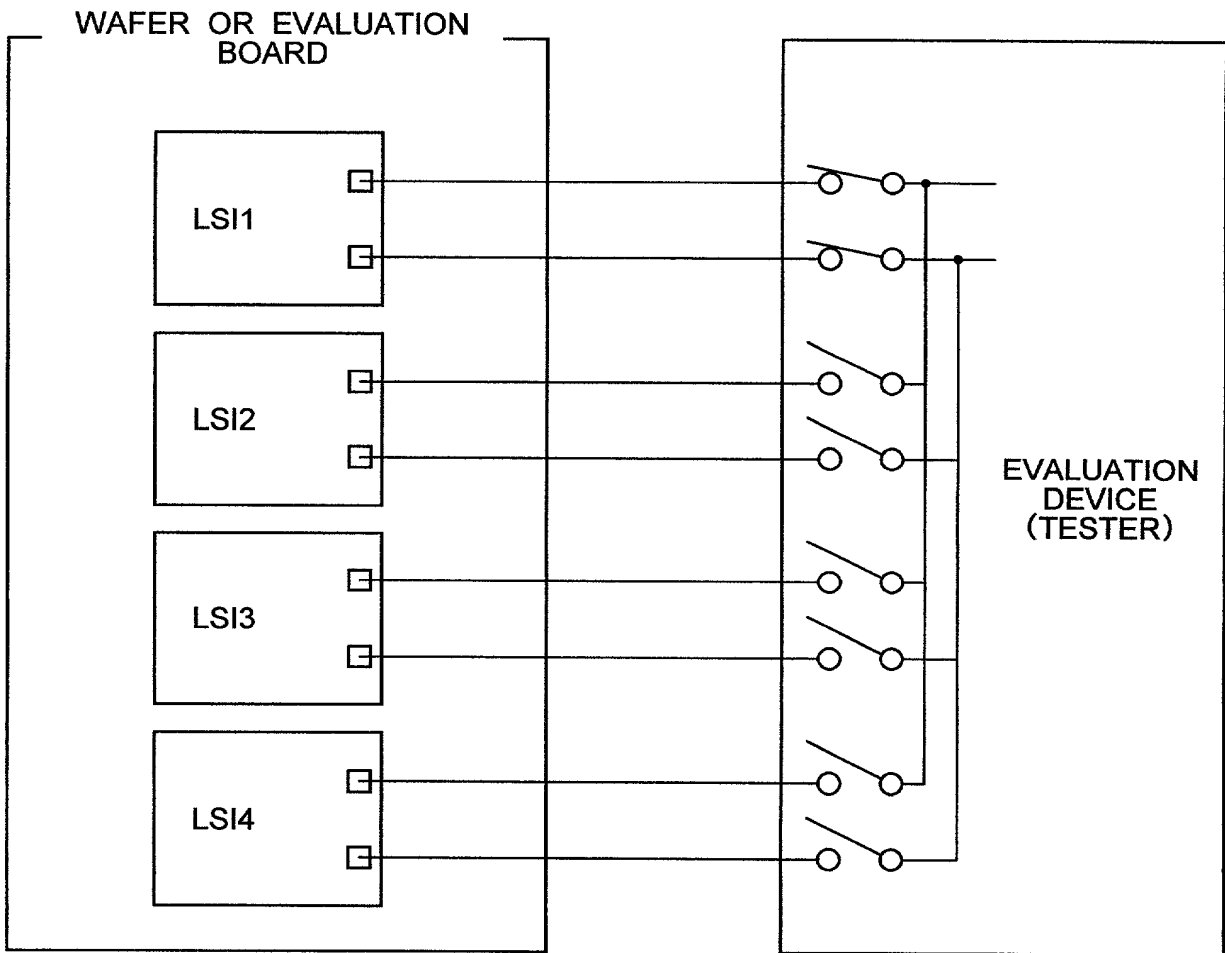


FIG. 10

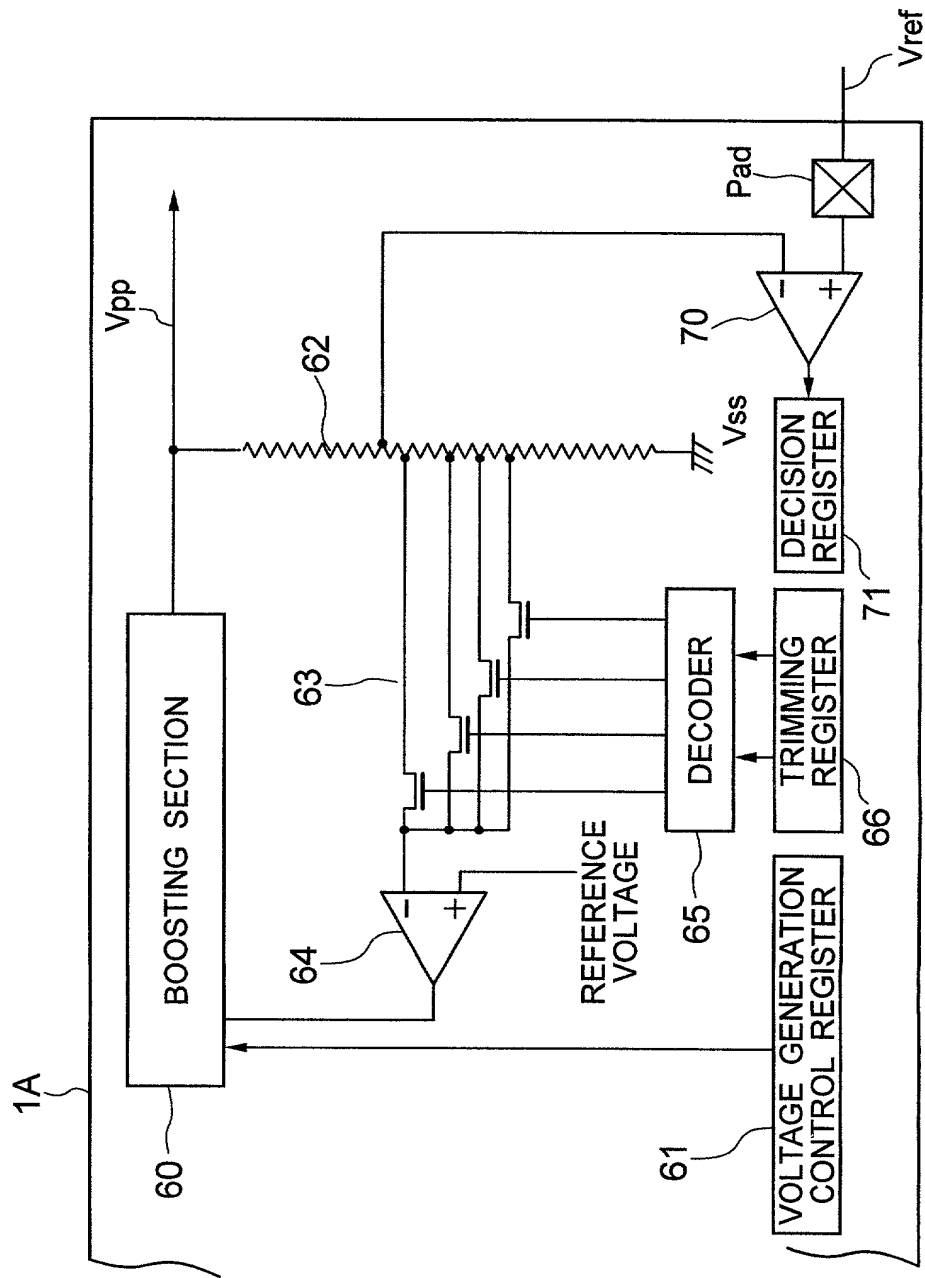


FIG. 11

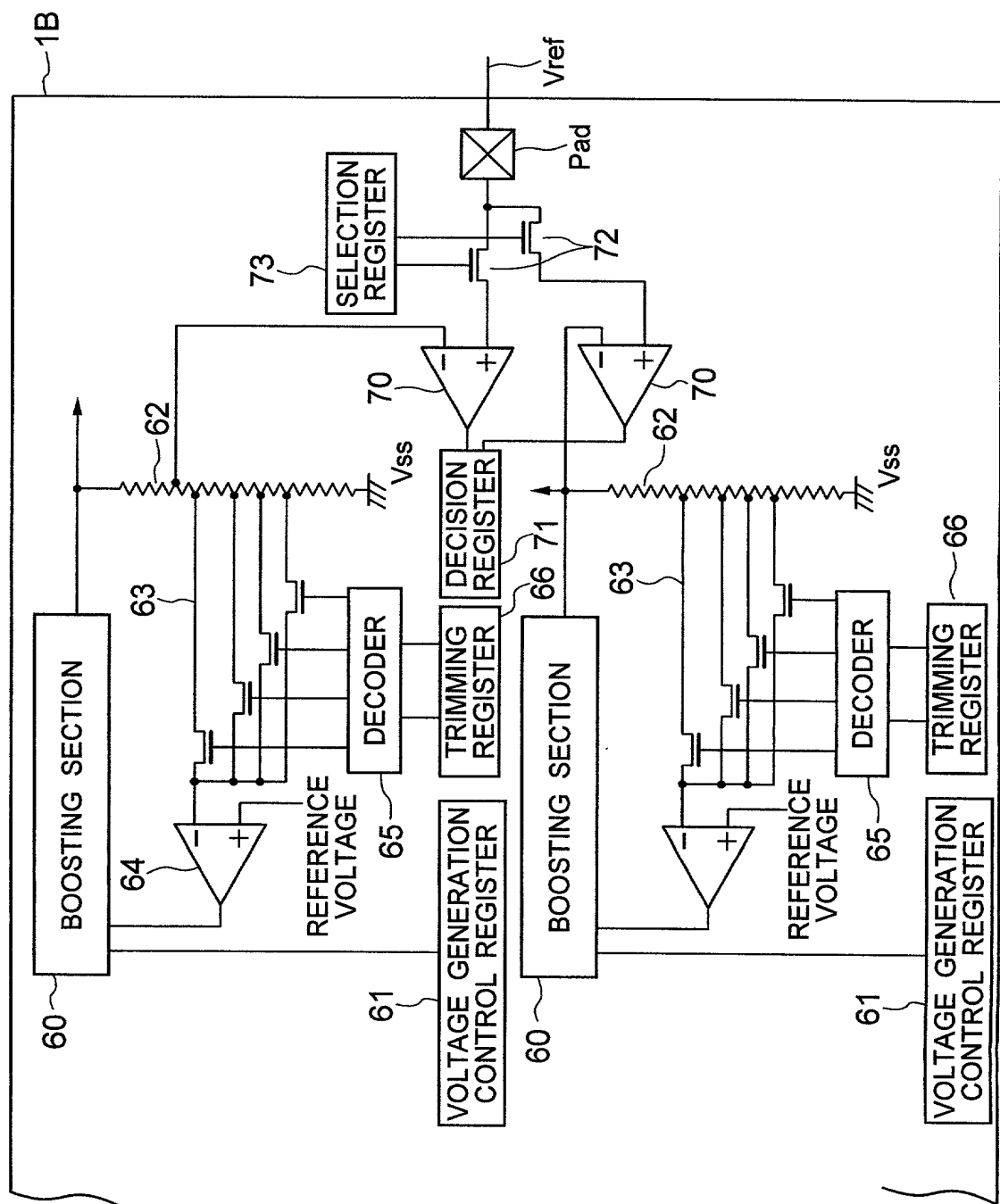


FIG. 12

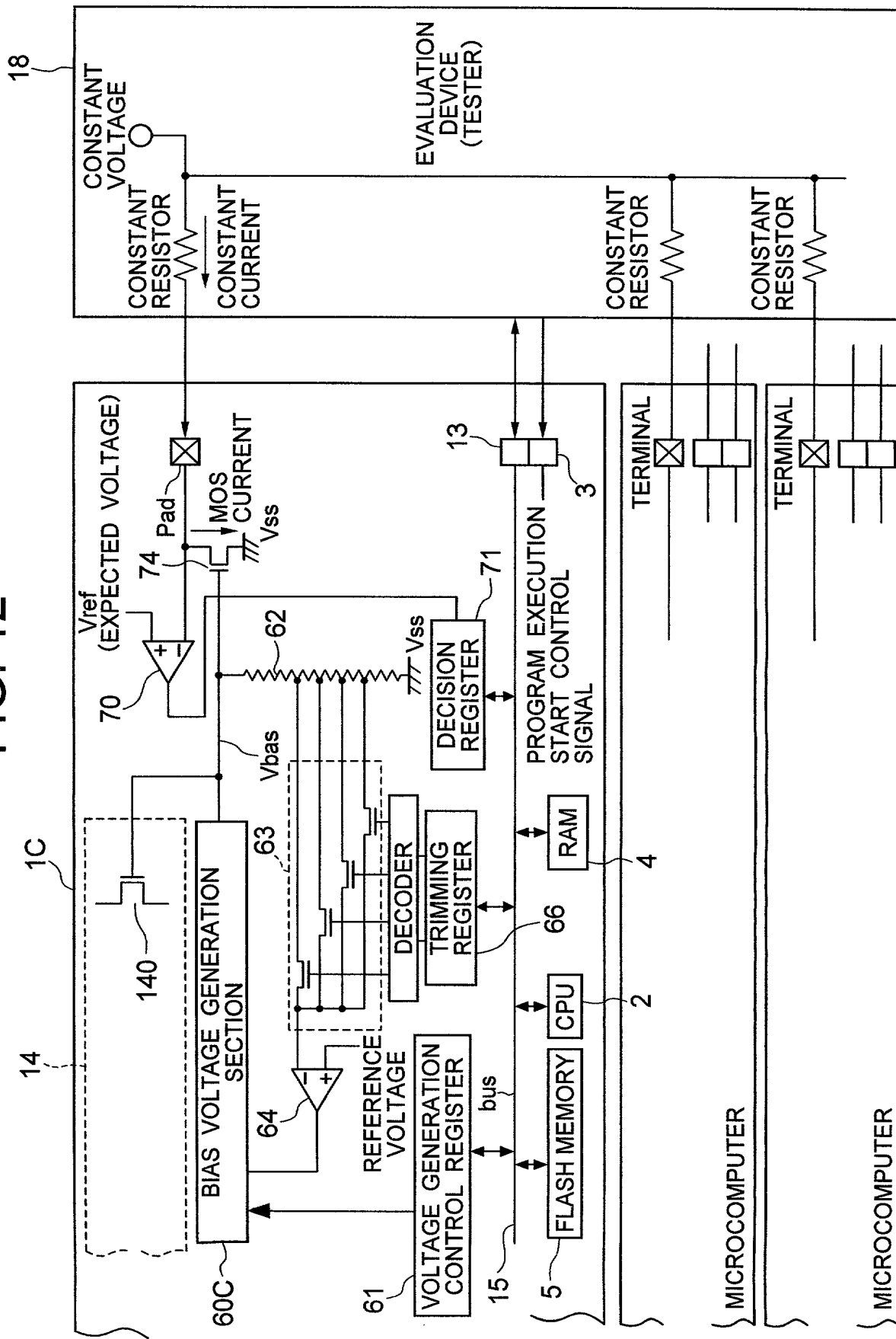


FIG. 13

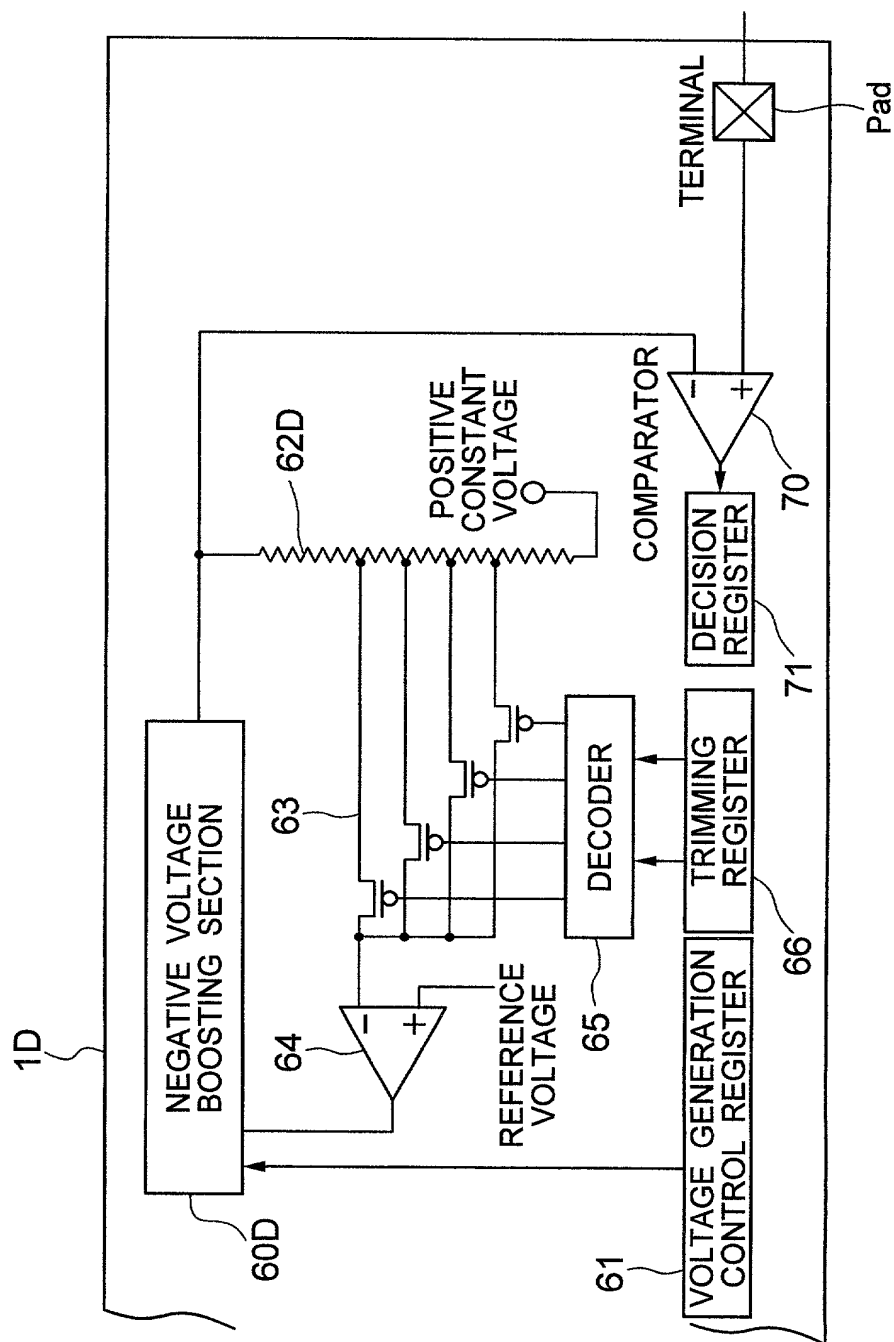


FIG. 14

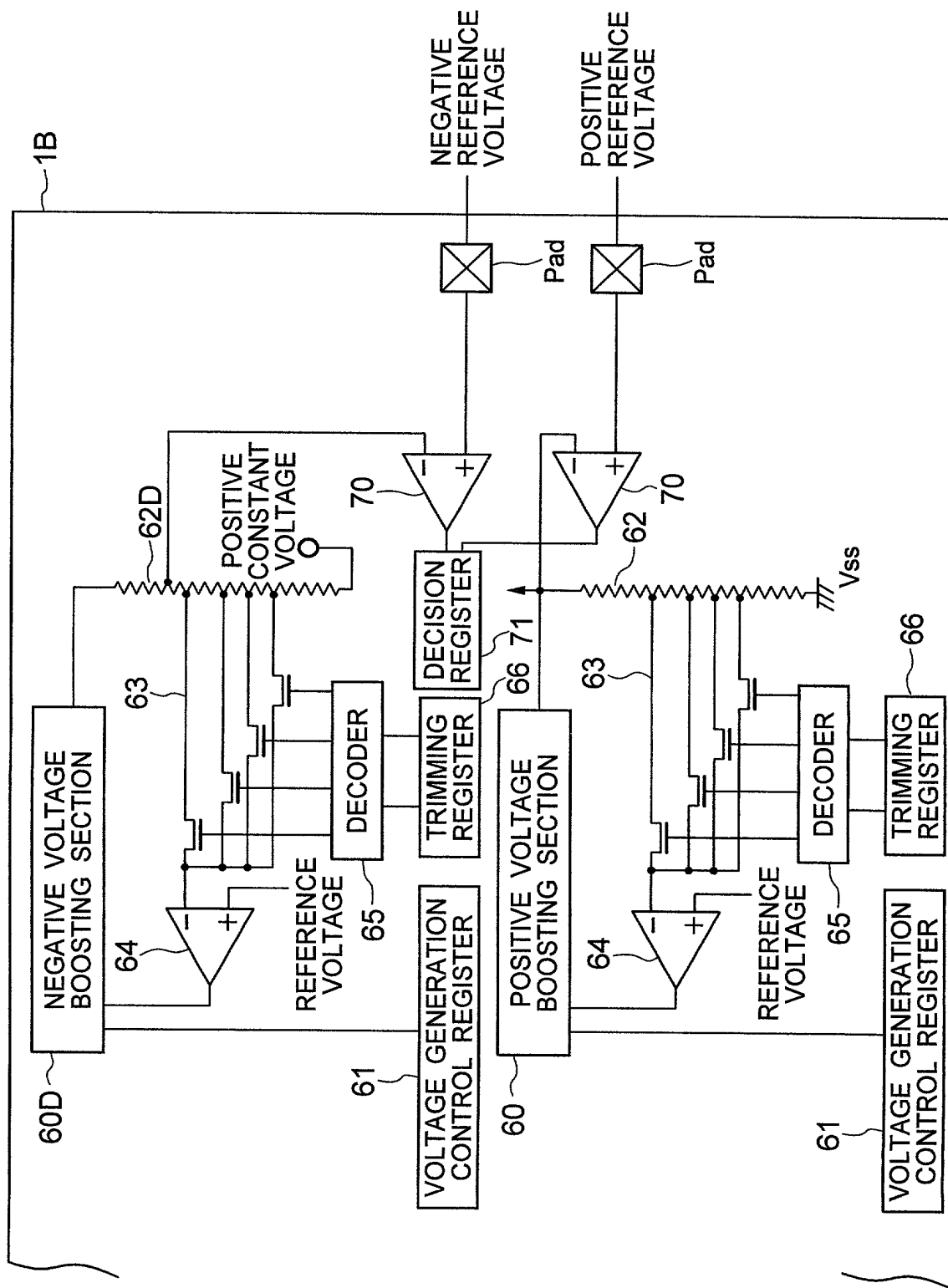


FIG. 15

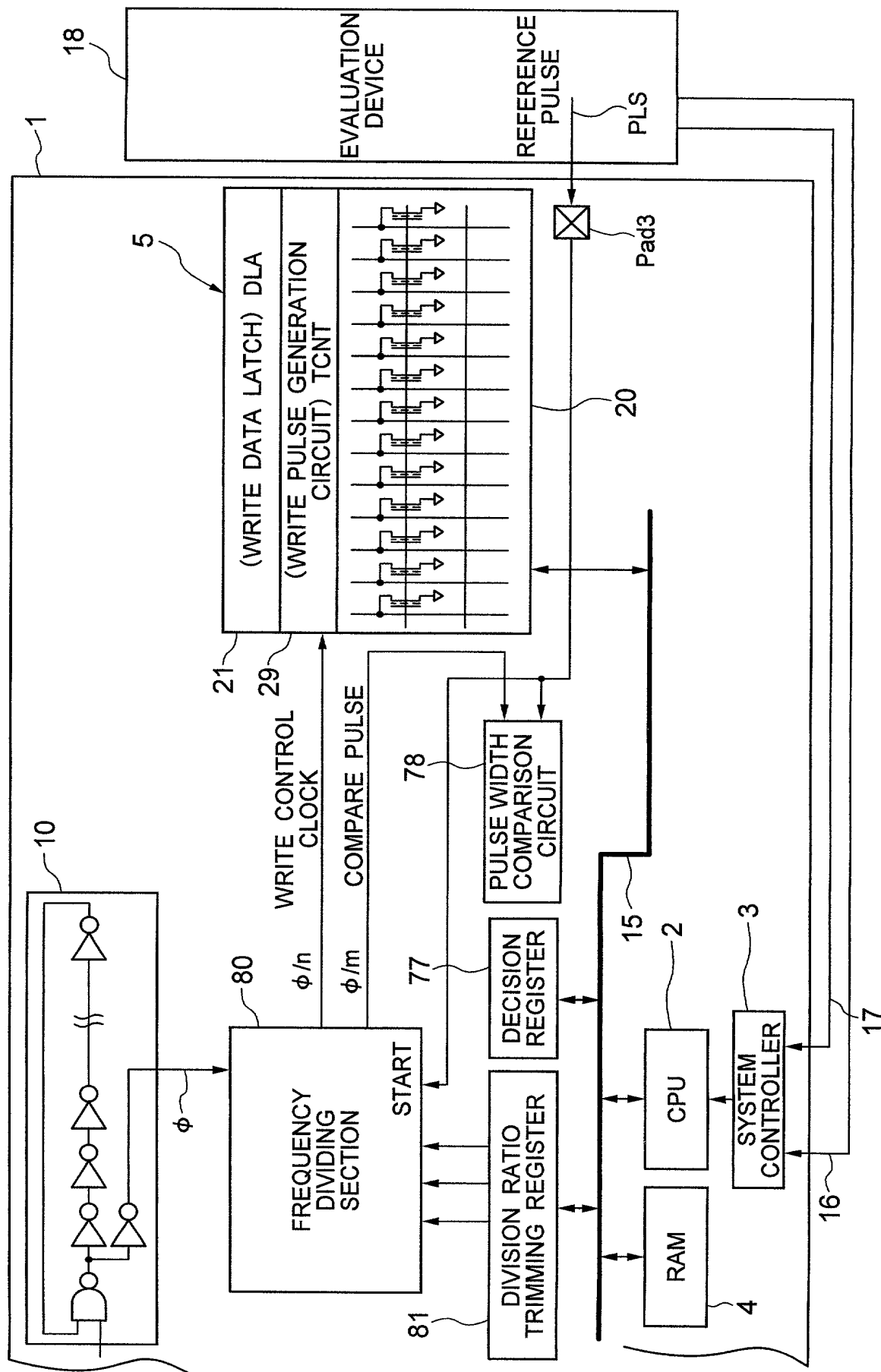


FIG. 16

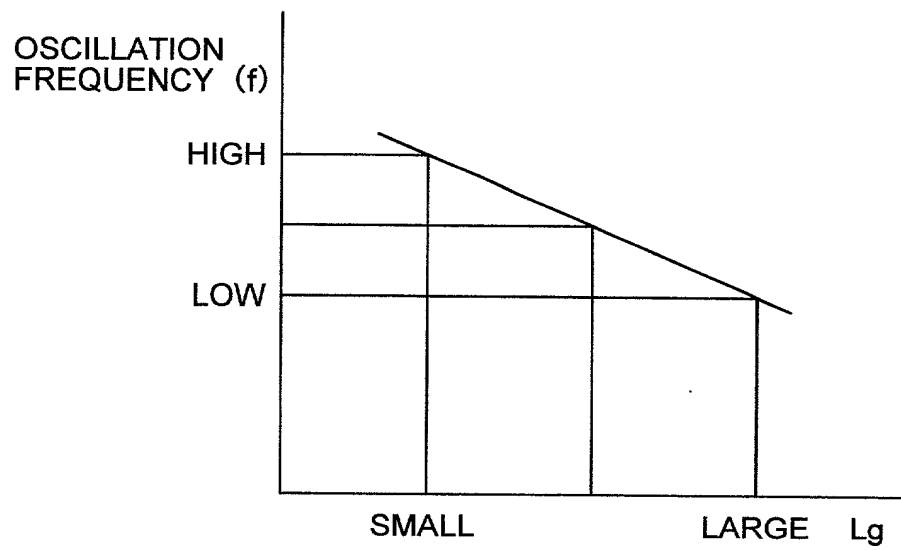








FIG. 19

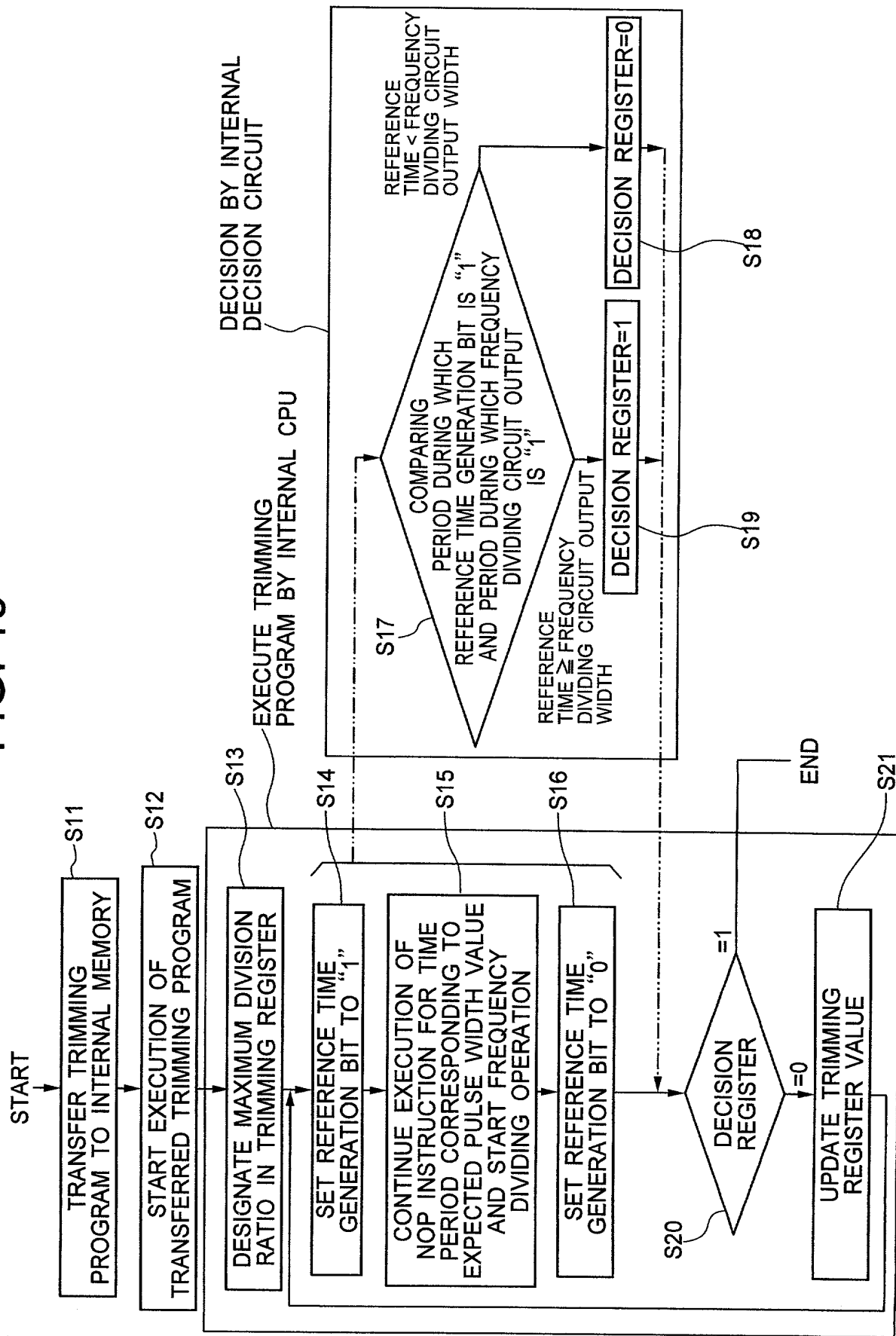
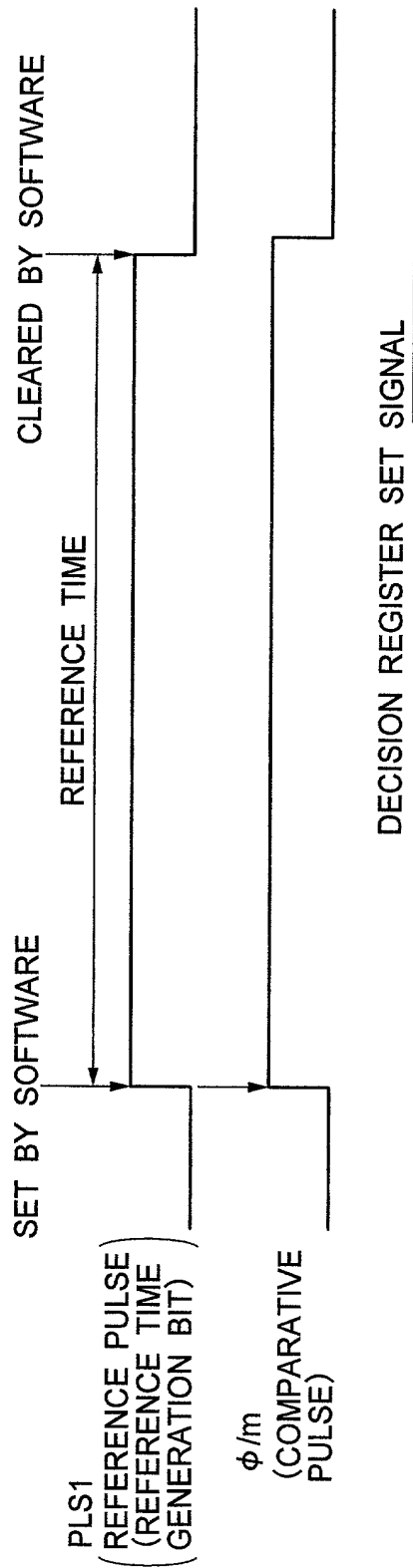


FIG. 20

RING OSCILLATOR FREQUENCY	TRIMMING		
	TRIMMING REGISTER	DIVISION RATIO	FREQUENCY ( $\phi/n$ ) AFTER TRIMMING
64MHz	(0,0,0)	1/64	64/64=1.0MHz
60MHz	(0,0,1)	1/60	60/60=1.0MHz
56MHz	(0,1,0)	1/56	56/56=1.0MHz
52MHz	(0,1,1)	1/52	52/52=1.0MHz
48MHz	(1,0,0)	1/48	48/48=1.0MHz
44MHz	(1,0,1)	1/44	44/44=1.0MHz
40MHz	(1,1,0)	1/40	40/40=1.0MHz
36MHz	(1,1,1)	1/36	36/36=1.0MHz

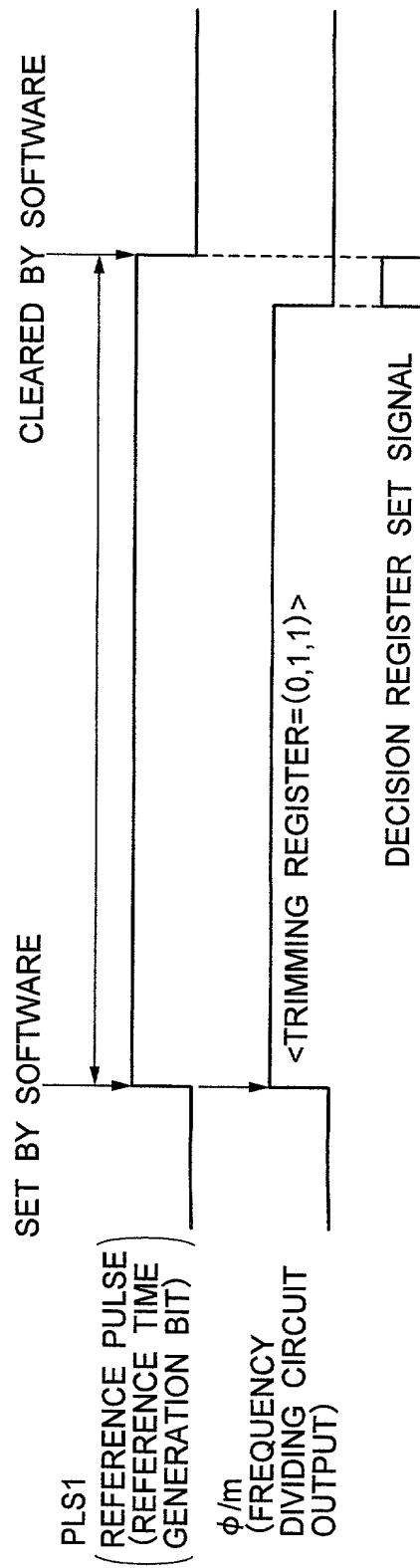
# FIG. 21A

MAXIMUM DIVISION RATIO IS DESIGNATED IN TRIMMING REGISTER <TRIMMING REGISTER=(0,0,0)>



# FIG. 21B

VALUE IN TRIMMING REGISTER IS UPDATED <TRIMMING REGISTER=(0,1,1)>



# FIG. 22

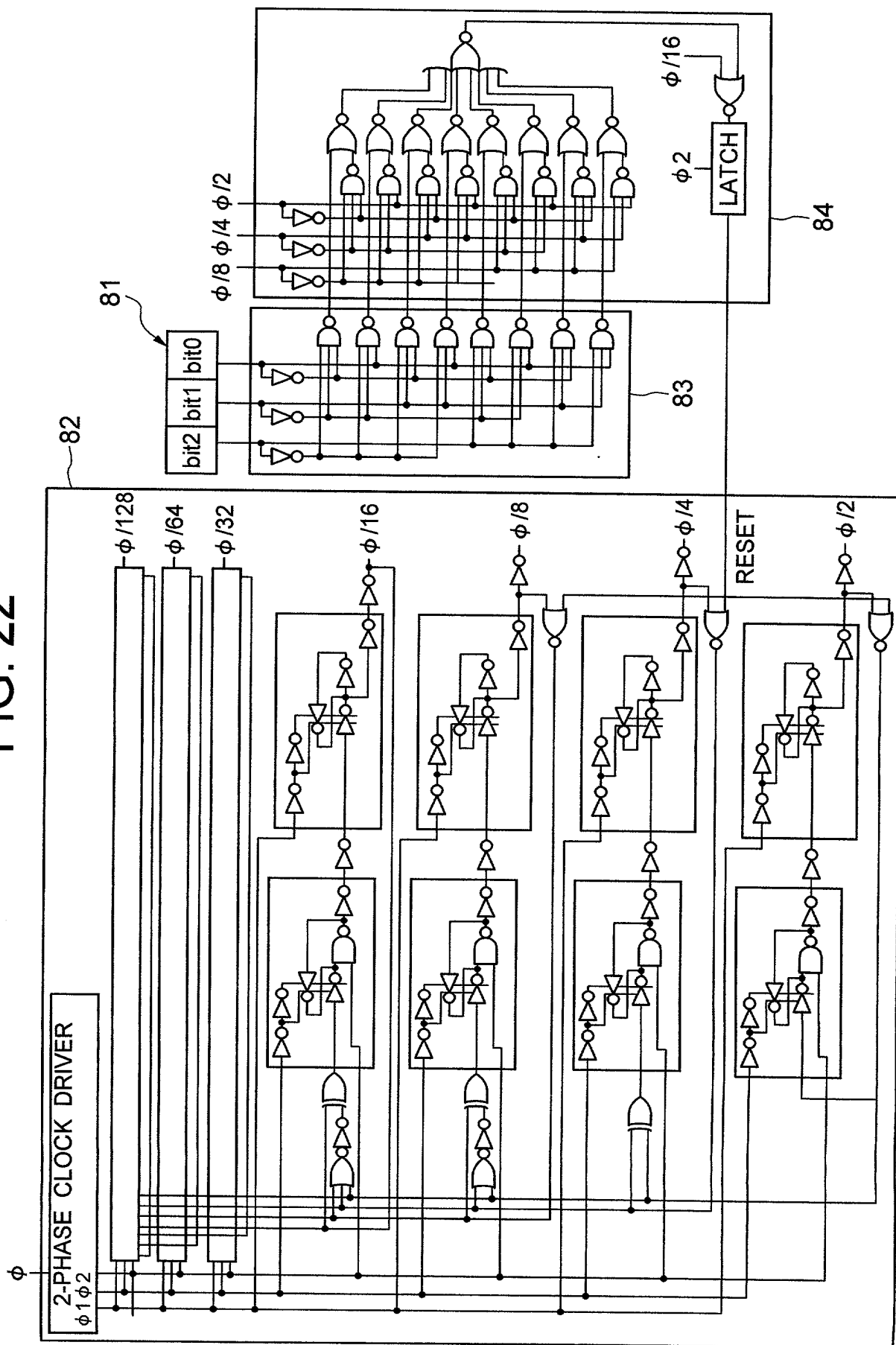


FIG. 23

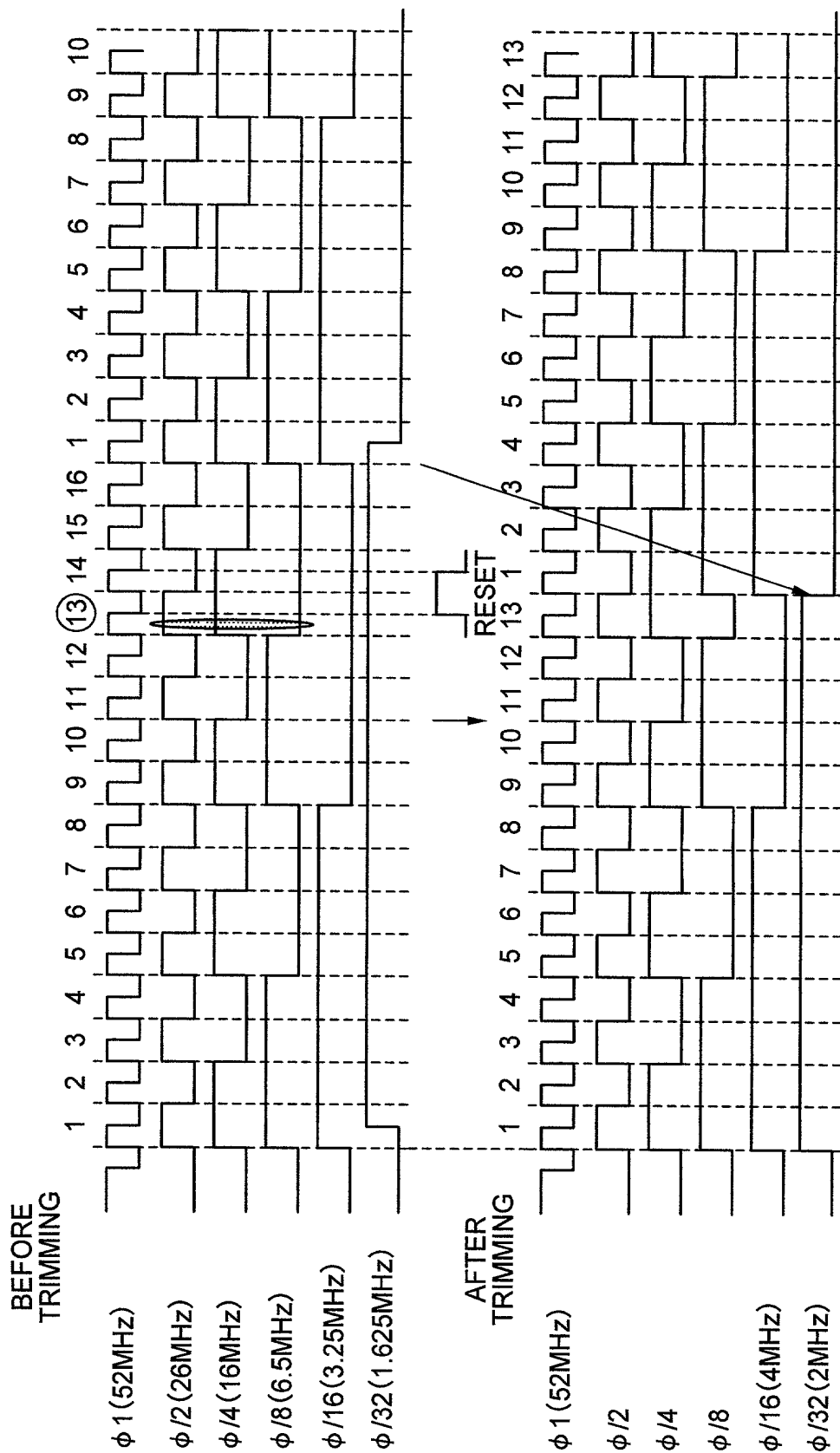
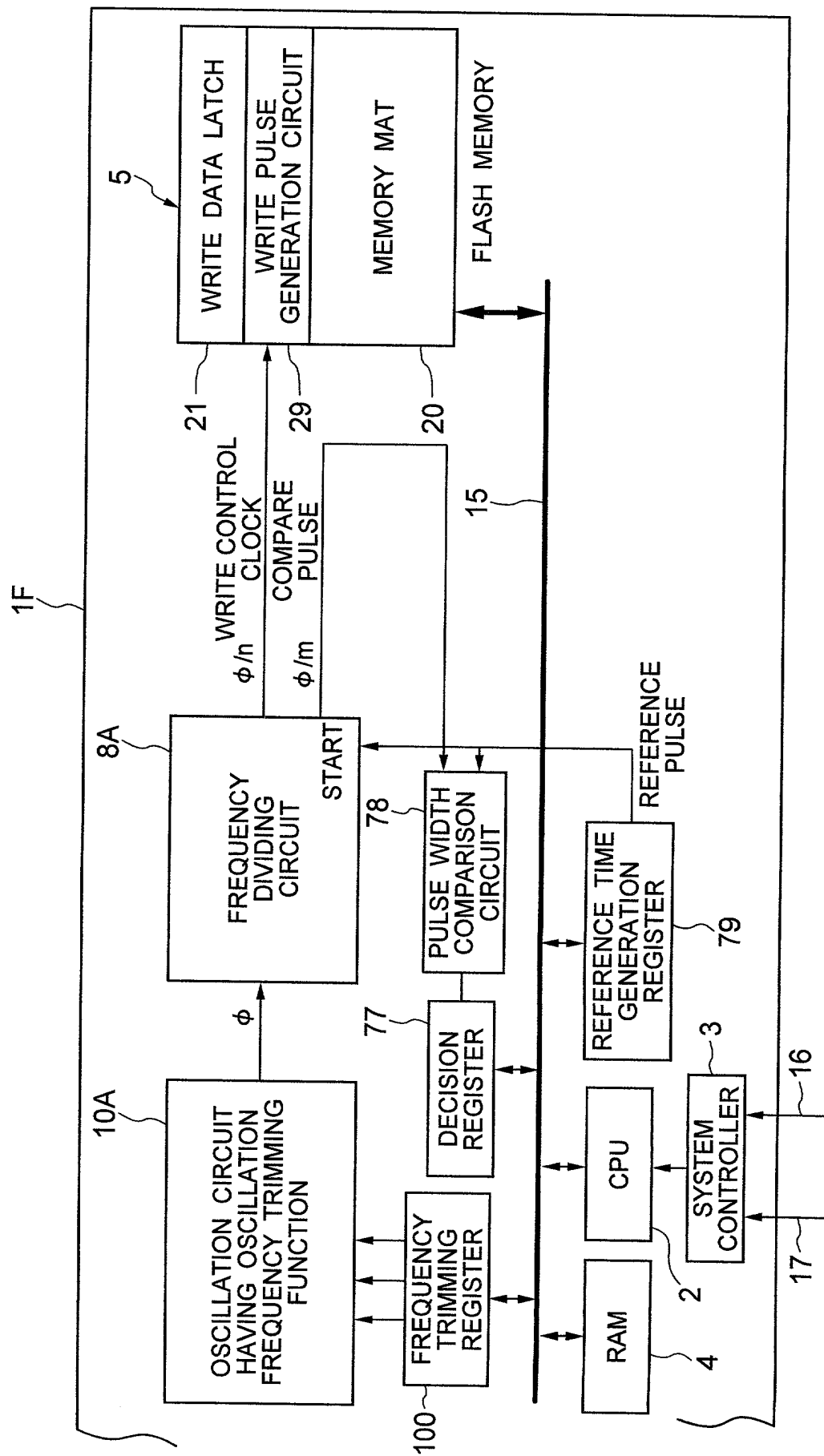


FIG. 24





# FIG. 25

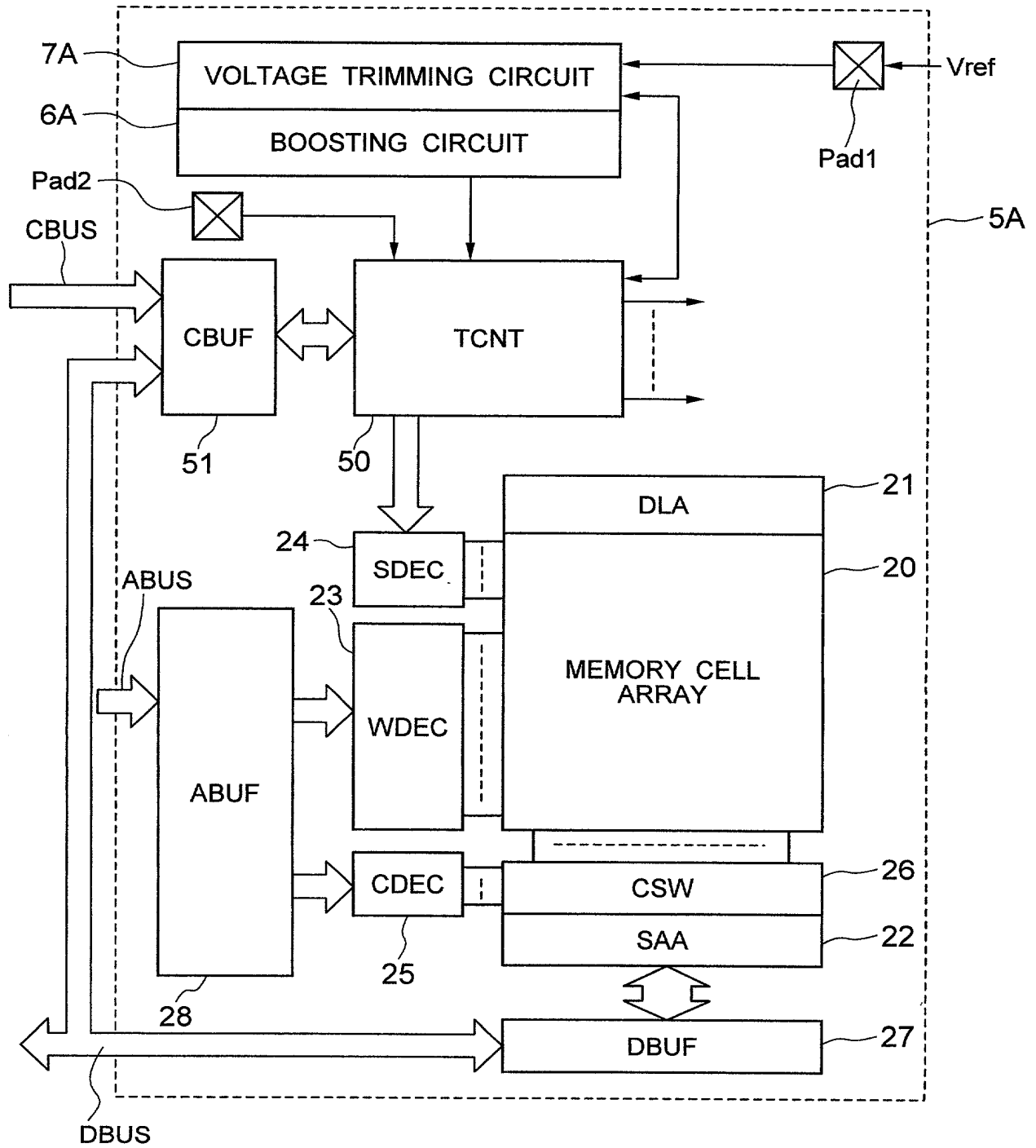


FIG. 26

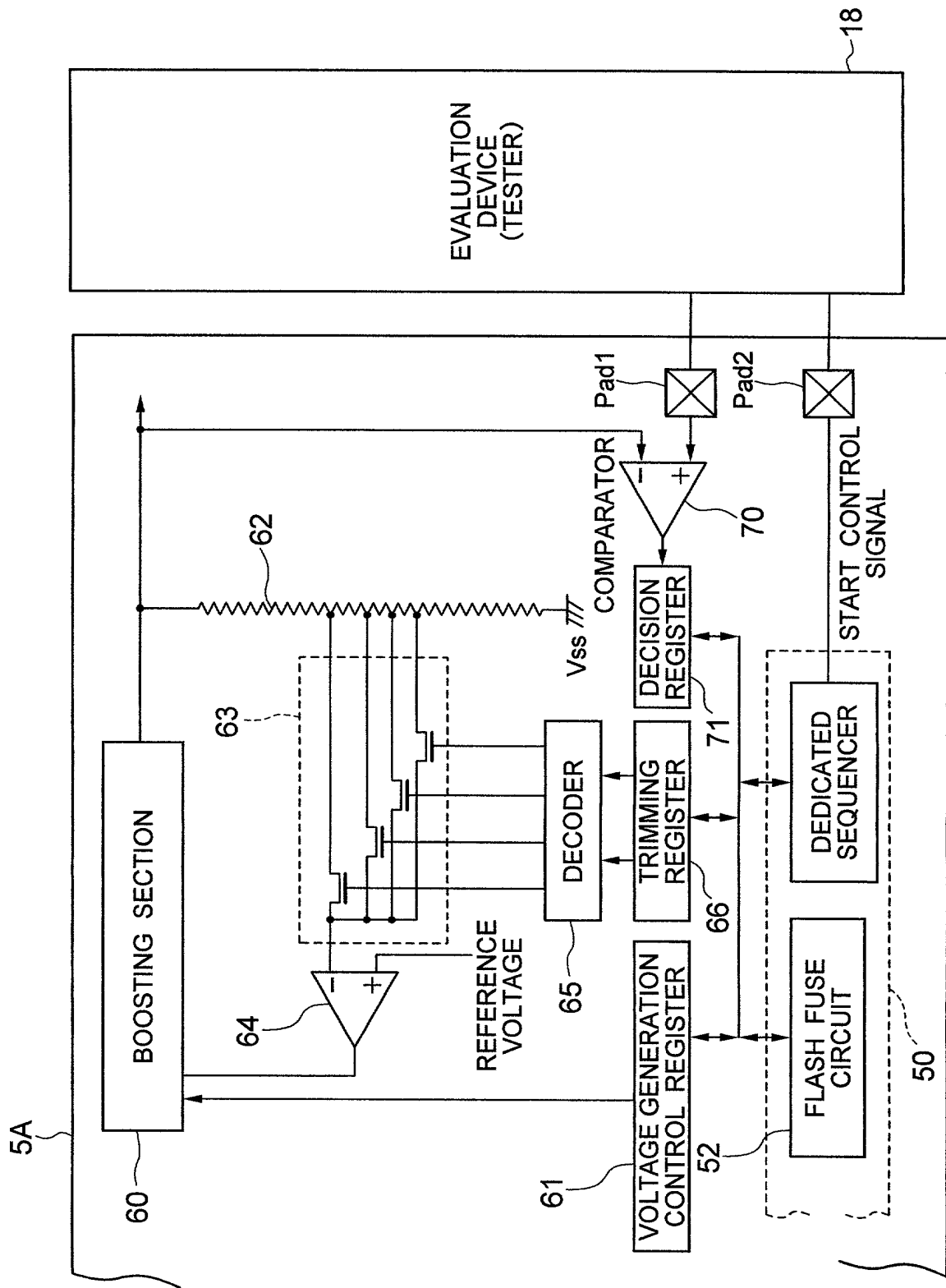


FIG. 27

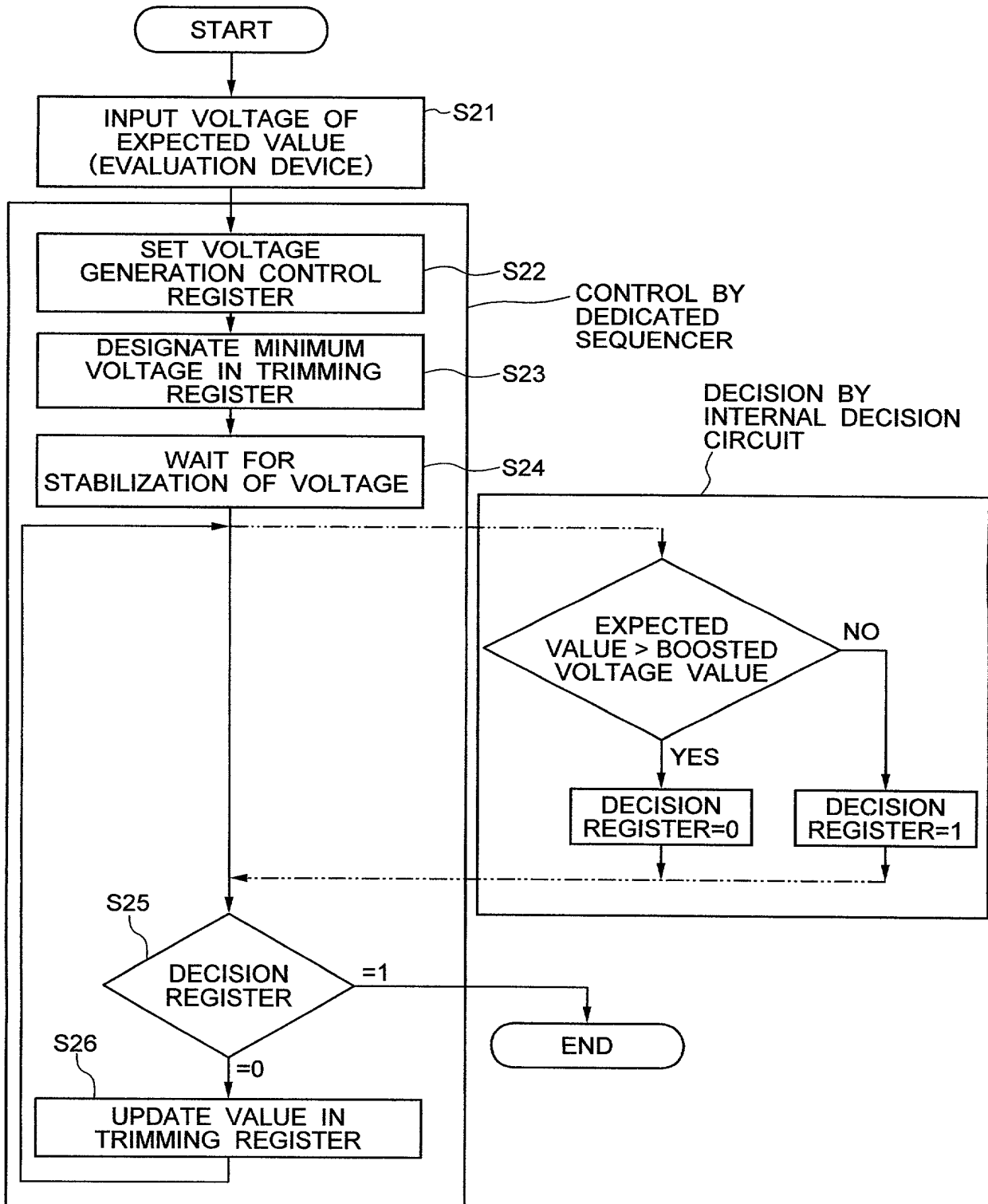


FIG. 28

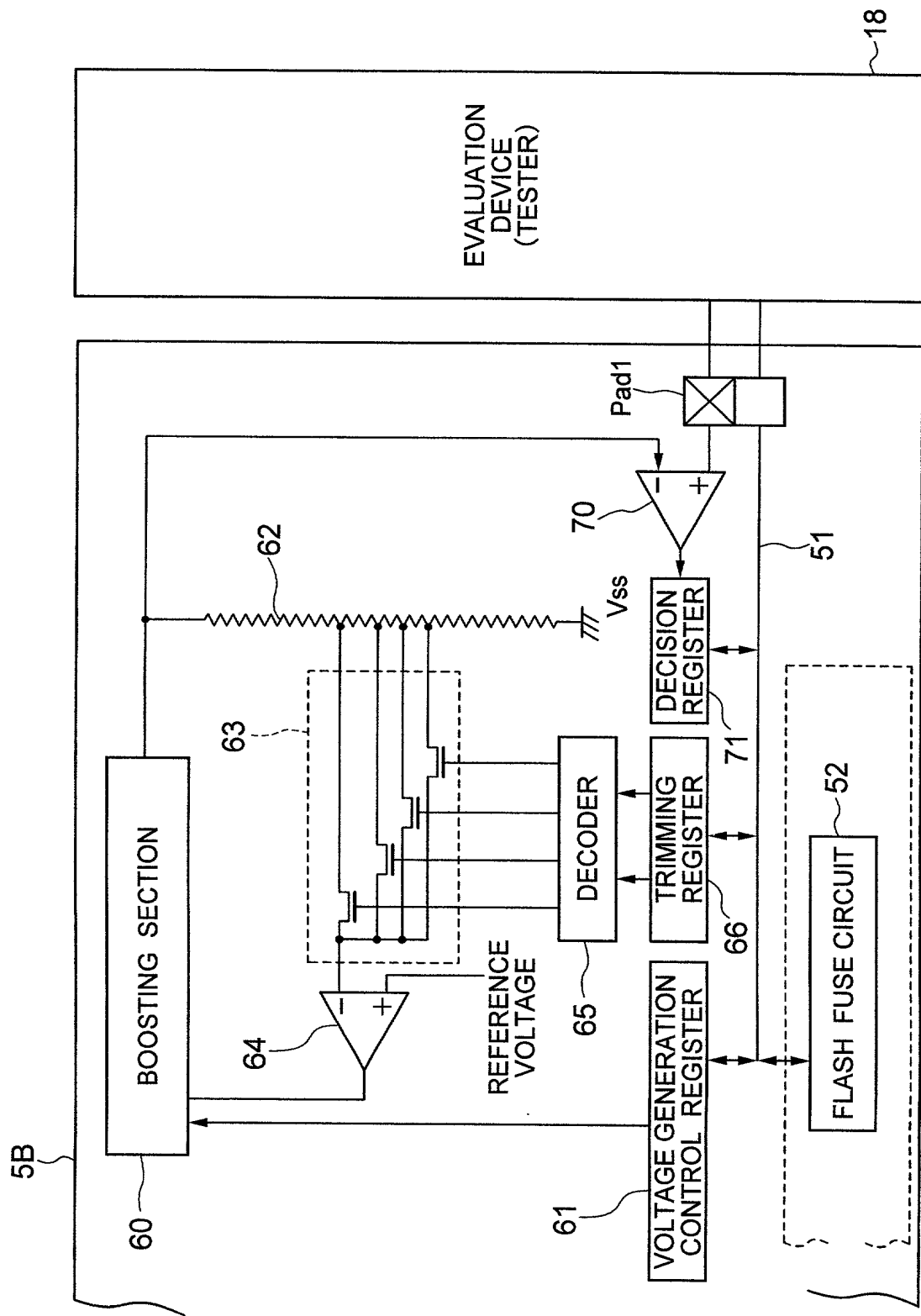


FIG. 29

